



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Like the Application of:

MOHAMMAD ABDALLAH, ET AL.

Application No.: 09/852,217

Filed: May 8, 2001

For: **Executing Partial-Width Packed Data Instructions**

Art Group: 2183

Examiner: Coleman, Eric

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

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In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Reexamination Continuation Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

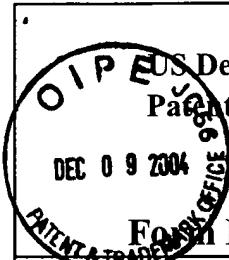
Date: 12-6-04

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Krista Mathieson Dec. 6, 2004
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	Atty. Docket No.:	Application No.:
	42390.P5193C	09/852,217
	Applicant:	Abdallah et al.
	Filing Date:	May 8, 2001

US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub-Class	Filing Date
	07/31/90	4,945,479	Rusterholz et al.			
	11/20/90	4,972,362	Elkind et al.			
	09/10/91	5,047,975	Patti et al.			
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	11/30/92	5,161,247	Murakami et al.			
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	02/14/95	5,390,135	Lee et al.			
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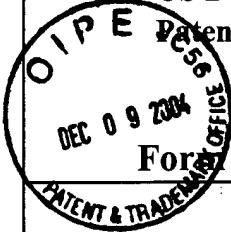
Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub-Class	Translation
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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner	Date Considered
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	Atty. Docket No.:	Application No.:
	042390.P5193C	09/852,217
	Applicant:	Abdallah et al.
	Filing Date:	May 8, 2001

US Patent Documents

Examiner's Initials		Date	Document Number	Name	Class	Sub-Class	Filing Date

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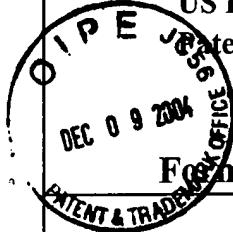
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	Santoro, Mark; Design and Clocking of VLSI Multipliers. Technical Report No. CSL -TR-89-397, October 1989. Pages i-xii and 1-118.
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 <p>US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)</p>	Atty. Docket No.: 042390.P5193C	Application No.: 09/852,217
	Applicant: Abdallah et al.	
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US Patent Documents

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	Undy, Steve, et al.; A Low-Cost Graphics and Multimedia Workstation Chip Set. April 1994, IEEE micro, pages 10 - 22.
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